What is claimed is:

1. A PDA, wherein the PDA outputs a current within a first current range to an external device when supplied with a battery power and outputs a current within a second current range to the external device when supplied with external power through an adapter, comprising:

a main device, for performing necessary data processing, enabling a control signal when supplied with external power through the adapter;

a switch device comprising a first input terminal receiving battery power, a second input terminal receiving external power, a control terminal receiving the control signal, and an output terminal outputs battery power when the control signal is disabled and outputs external power when the control signal is enabled;

a current limiting device coupled to the output terminal of the switch device, wherein the current limiting device outputs a current within the first current range to the external device when the switch device outputs battery power, and outputs a current within the second current range to the external device when the switch device outputs external power.

2. The PDA as claimed in claim 1, the current limiting device further comprising:

an impedance device for providing a first impedance and a second impedance, wherein the impedance device outputs the first impedance when the control signal is disabled and outputs the first and the second impedances when the control signal is enabled; and

a current limiting module coupled to the output terminal of the switching device, wherein the current limiting module outputs the current within the first current range when receiving the first impedance and outputs the current within the second current range when receiving the first and the second impedances.

3. The PDA as claimed in claim 2, wherein the external device is a printer.

4. The PDA as claimed in claim 1, wherein the external device is a digital camera.

5. The PDA as claimed in claim 2, wherein the current limiting module is a current limiting integrated circuit(IC) MIC2544 or MIC2548.

6. The PDA as claimed in claim 5, wherein a fourth pin of the current limiting integrated circuit is coupled to the impedance and wherein a maximum value of an output current (I_{Limit}) of the current limiting integrated circuit is limited by the impedance (R_{SET}) provided by the impedance device in accordance with the formula $I_{Limit} = \frac{230V}{R_{SET}}$.

7. The PDA as claimed in claim 2, the PDA and the external device are connected by a cable.

8. The PDA as claimed in claim 2, wherein the first current range is smaller than the second current range.

9. The PDA as claimed in claim 2, wherein the impedance device comprises:

a first resistor, coupled between the current limiting module and a voltage level (Gnd), for providing the first impedance;

a second resistor for providing the second impedance; and

a selecting device serially connected to the second resistor, wherein both the selecting device and the second resistor are coupled between the current limiting device and the voltage level (Gnd), wherein the selecting device is turned off and the limiting module receives only the first impedance when the control signal is disabled, and wherein the selecting device is turned on and the limiting module receives the first and the second impedances when the control signal is enabled.

10. The PDA as claimed in claim 9, wherein the selecting device is an N-type transistor having a gate receiving the control signal, a drain coupled to the second resistor, and a source coupled to the voltage level (Gnd).

11. The PDA as claimed in claim 1, wherein the selecting device is a P-type transistor having a gate receiving the control signal, a drain coupled to the voltage level (Gnd), and a source coupled to the second resistor.

12. A current limiting device built into a PDA, wherein the PDA is used as a host and is connected to an external device, wherein the current limiting device outputs a current within a first current range to the external device when the PDA is supplied with battery power, and wherein the PDA enables a control signal and the current limiting device outputs a current within a second current range to the external device when the PDA is supplied with external power through an adapter, comprising:

a switch device comprising a first input terminal receives battery power, a second input terminal receives external power, a control terminal receives the control signal, and an output terminal outputs battery power when the control signal is disabled and outputs external power when the control signal is enabled;

a current limiting module coupled to the output terminal of the switching device;

a first resistor, coupled between the current limiting module and a voltage level (Gnd), to provide a first impedance;

a second resistor for providing a second impedance; and

a selecting device serially connected with the second resistor, wherein both the selecting device and the second resistor are coupled between the current limiting device and the voltage level (Gnd), wherein the selecting device is turned off and the limiting module receives only the first impedance when the control signal is disabled, and wherein the selecting device is turned on and the limiting module receives the first and the second impedances when the control signal is enabled.

13. The current limiting device as claimed in claim 12, wherein the current limiting module is a current limiting integrated circuit MIC2544 or MIC2548.

14. The current limiting device as claimed in claim 12, wherein the first current range is smaller than the second current range.

15. The current limiting device as claimed in claim 13, wherein a fourth pin of the current limiting integrated circuit is coupled to the impedance and wherein a maximum value of an output current (I_{Limit}) of the current limiting integrated circuit is limited by the impedance (R_{SET}) provided by the impedance device in accordance with a relationship formula $I_{Limit} = \frac{230V}{R_{SET}}$.

16. The current limiting device as claimed in claim 12, wherein the selecting device is an N-type transistor having a gate receiving the control signal, a drain

coupled to the second resistor, and a source coupled to the voltage level (Gnd).

17. The current limiting device as claimed in claim 12, wherein the selecting device is a P-type transistor having a gate receiving the control signal, a drain coupled to the voltage level (Gnd), and a source coupled to the second resistor.